



Smart Solutions for Cost Reduction and Yield Enhancement

Q-Star Test and **Ridgetop Group Inc** invite you to meet us at the Silicon Valley Test Conference and learn about our new **solutions for Cost Reduction and product Quality Improvement** as well as for **Yield Enhancement and advanced BGA/FPGA testing**. We are welcoming you at our event booth.

We also invite you to come and join the comprehensive **half day Tutorial** on "**Getting the Best out of Current Test**" which will be given by **Q-Star's CEO Dr Hans Manhaeve** on Tuesday November 9th 2010 in the afternoon and address all aspects of IDDX/ISSX you need to know for a successful application of the related test methodologies to today's low power as well as high current devices.

If you are interested in reducing test costs and improving product yields, then our solutions can serve you. We offer a total package solution consisting of test program optimization, engineering & conversion, IDDX/ISSX application support and electronic health monitoring. We assist you in achieving the required test coverage and lowering your test costs based on our unique high performance supply/ground current test solutions. We offer you the right solution to assure quality and reliability of your products and to test your high performance DSM and Nanoscale ICs as well as your low power semiconductor devices.



Discover the rewards of our Solutions:

- Up to **10x** test time reduction without test quality loss
- Supporting **Oppb** quality & reliability requirements
- Fast & accurate Failure Analysis

We are looking forward to meeting you in San Jose, Ca. Contact us at events@qstar.be to schedule a meeting and remember: "Real *surprises* await those who choose wisely".

Q-Star Test is a leading supplier of innovative solutions for test cost reduction and product quality improvement, offers best in class intelligent IDDX and ISSX measurement equipment, DFT and Test consulting and engineering services, Electronic health management and Prognostic solutions, as well as electronic and PCB engineering services for the development of prototypes and small series productions.

Ridgetop Group Inc is a provider of advanced diagnostic/prognostic and yield enhancement solutions. Since its founding in 2000, Ridgetop has introduced innovative BIST solutions for BGA package testing and IC yield improvement through patented, in-situ test structures.

www.qstar.be

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